

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor Application of: Ming-i M. Lin

Serial No.: 10/771,823

Filing Date: 02/03/2004

Art Unit:

Dkt. No.: END920030080US1

Title: MULTILEVEL PARALLEL CRC GENERATION AND CHECKING CIRCUIT

Commissioner for Patents

Office of Initial Patent Examination

Filing Receipt Corrections

P.O. Box 1450

Alexandria, VA 22313-1450

CORRECTION OF FILING RECEIPT

Sir:

Applicants respectfully request correction of the filing receipt for this case as indicated on the attached copy of the filing receipt.

Consideration of the foregoing in relation to this patent application is respectfully requested.

Respectfully submitted,

Jack Friedman

Reg. No. 44,688

Date: 05-11-2004

SCHMEISER, OLSEN & WATTS

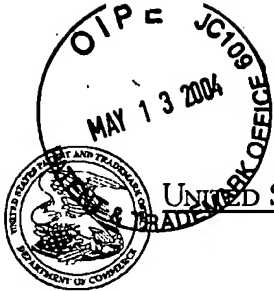
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/771,823	02/03/2004	2133	950	END920030080US1	5	30	3

CONFIRMATION NO. 3975

30449
SCHMEISER, OLSEN + WATTS
SUITE 201
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LATHAM, NY 12033

FILING RECEIPT



OC000000012533878

Date Mailed: 05/05/2004

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Assignment For Published Patent Application

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Domestic Priority data as claimed by applicant

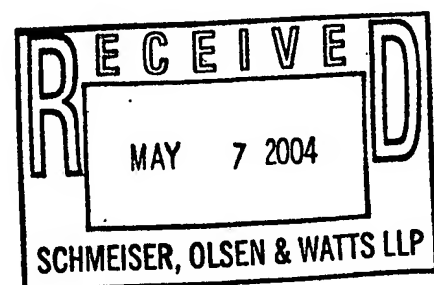
Foreign Applications

If Required, Foreign Filing License Granted: 05/05/2004

Projected Publication Date: 08/04/2005

Non-Publication Request: No

Early Publication Request: No



Title

Multilevel parallel CRC generation and checking circuit

Preliminary Class

714

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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